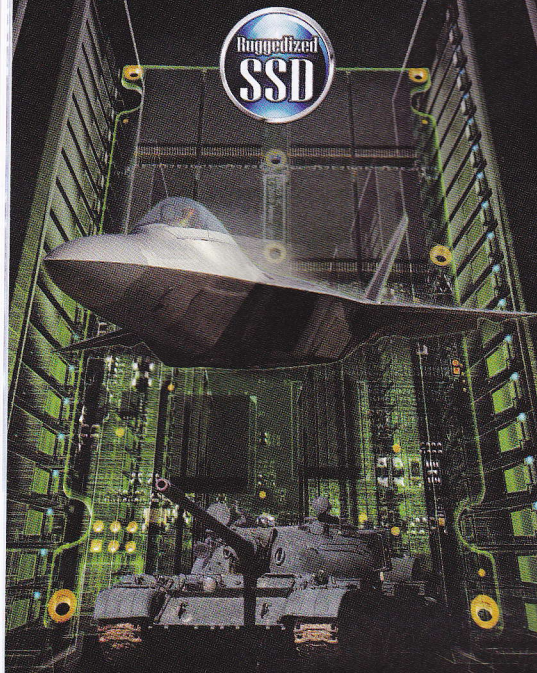
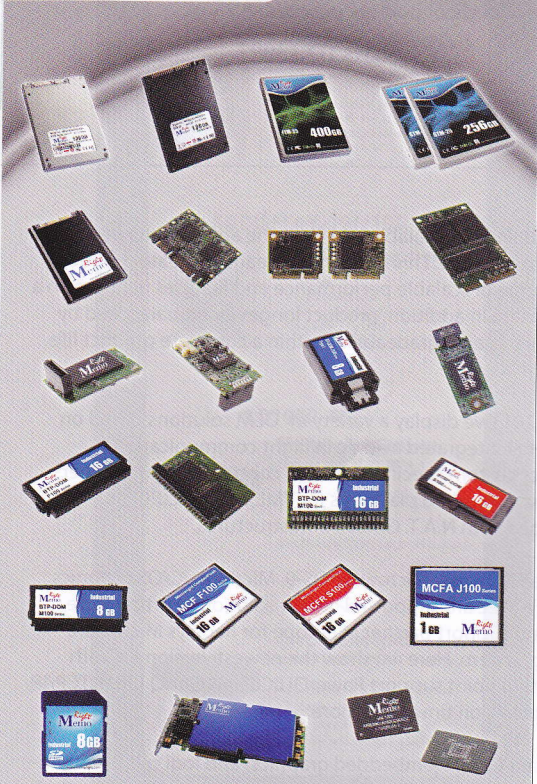


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**EMBEDDED WORLD HIGHLIGHTS**

**Hall-Stand 10-343**

**Keil: multi-core debugging for NXP LPC4000**

The new NXP LPC4000 devices integrate a multi-core system consisting of an ARM Cortex-M4 and a Cortex-M0 processor. This asymmetric multi-core system requires additional features from the development tools, in particular the debugger. In the JTAG mode a LPC4000 system provides simultaneous access to the two Cortex-M processors using the ARM CoreSight debug/test interface. The Keil ULINKpro Debug and Trace Unit together with the Keil MDK-ARM enables two independent instances of the µVision Debugger to be run simultaneously.

[News ID 13088](#)

**Hall-Stand 12-244**

**Digi: multi-function wireless programmable terminal server**

**Digi International** introduces the ConnectPort LTS multi-function wireless programmable terminal server. The ConnectPort LTS provides serial-to-IP connectivity and optional ZigBee wireless communications. This minimizes equipment space requirements and reduces cost by combining a terminal server and wireless gateway in one device. The ConnectPort LTS is ideal for utilities, security, building automation, retail, industrial automation, health-care, data center management and other applications.

[News ID 13080](#)

**Hall-Stand 11-118**

**Wind River: VxWorks RTOS selected by biopharmaceutical manufacturer**

Wind River announces that Fedegari Autoclavi has standardized the control system for its industrial machine line, called Thema4, on Wind River's VxWorks RTOS. This meets the diverse needs of the pharmaceutical industry, including compliance with global certification requirements set out by the United States Food and Drug Administration and the European Union good manufacturing practices guide.

[News ID 13079](#)

**Hall-Stand 12-313**

**EKF: CompactPCI PlusIO rear I/O transition module**

The PICMG 2.30 CompactPCI PlusIO specification defines standardised high speed rear I/O across the backplane connector J2, such as PCI Express, SATA, and Gigabit Ethernet. The PRI-RIO is a rear I/O transition module for PlusIO systems, provided with back panel connectors for external usage, and also connectors for system internal cabling. The PRI-RIO can be used together with any PlusIO compliant

CPU board, e.g. the PCI-GROOVE. The PRI-RIO uses a special CPCI J2 connector, suitable for high speed signals. Two Gigabit Ethernet RJ45 ports are available from the PRI-RIO back panel, and in addition 2 x USB/eSATA via combo connectors. For system internal usage the PRI-RIO is provided with 2 x SATA and 2 x USB connectors. In addition, the PRI-RIO can be equipped with up to four connectors according to the PCI Express External Cabling Specification.

[News ID 13078](#)

**Hall-Stand 9-463**

**Gleichmann: dual IO-Link master transceiver with integrated UARTs**

The E981.12 designed by Elmos, distributed by Gleichmann Electronics, is a dual IO-Link master transceiver enabling a scalable implementation of multi-port systems (2/4/8/16-port) regardless of the number of UARTs of the microcontroller in use. Due to the two UARTs integrated on the E981.12, for the first time users can use microcontrollers without integrated UARTs in cost-sensitive applications. This generally results in a significant reduction of the system costs. However, if UARTs integrated in the microcontroller already exist, these can obviously continue to be used.

[News ID 13075](#)

**Hall-Stand 12-436 / 11-308**

**TI: floating-point Piccolo MCUs for energy-efficient applications**

Bridging the gap between low-cost Piccolo and high-performance Delfino floating-point MCUs, Texas Instruments announces its new low-cost TMS320F2806x Piccolo floating-point MCUs with enhanced math engine specifically designed to simplify programming and optimize performance in real-time control applications that may require integrated communications. Developers of energy-efficient motor control and renewable energy applications can now use a single F2806x MCU to cost-effectively execute control loops as well as power line communications protocols and modulation schemes. Balancing performance with the integration and ease-of-use inherent to MCUs, the new F2806x MCUs also deliver a broader range of connectivity and memory options and are backed by TI's robust tools and free controlSUITE software.

[News ID 13072](#)

**Hall-Stand 12-306 / 12-568**

**Toshiba: free resources for development of ARM-based LINUX systems**

Toshiba Electronics Europe and open source development and integration specialist kernel concepts have announced a range of free re-